Abstracts

A versatile receiver IC supporting WCDMA, CDMA and AMPS cellular handset applications

K. Rampmeier, B. Agarwal, P. Mudge, D. Yates and T. Robinson. "A versatile receiver IC supporting WCDMA, CDMA and AMPS cellular handset applications." 2001 Radio Frequency Integrated Circuits (RFIC) Symposium 01. (2001 [RFIC]): 21-24.

This paper presents a single chip, superhetrodyne receiver IC capable of supporting dual-band, tri-mode handsets. Supported standards include W-CDMA, CDMA (IS-98-C) and AMPS. The receiver consists of multistep gain LNAs, high performance RF mixers with adjustable linearity, a variable gain amplifier, on chip VCO cores, and an IQ downconverter. The IC is fabricated in a 35 GHz f/sub t/ Si BiCMOS process and packaged into a 48 pin 7 mm/spl times/7 mm land grid array (RF-LGA/sup TM/) chipscale package.

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